



MUH-11618

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By: h p ~

Date: 3/17/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Udo Hartmann

Applic. No. : 09/915,984

Filed : July 25, 2001

Title : Apparatus for Testing Semiconductor Devices

Examiner : Paresh Patel

Group Art Unit : 2829

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the Office action dated December 16, 2002 kindly
amend the above-identified application as follows:

In the Claims:

Please cancel claims 1-8 and please add the following claims:

-- 9. A method for enabling semiconductor devices to be
tested, the method which comprises:

#11A Aust

M. Bauman

4/1/03

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